

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takahito NAKANO, et al.

SERIAL NO: 10/735,680

GAU: 2181

FILED: December 16, 2003

EXAMINER:

SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND CIRCUIT BOARD AND
INFORMATION READOUT METHOD



REQUEST FOR EXTENSION OF TIME
UNDER 37 C.F.R. 1.136

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

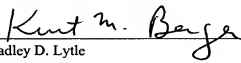
It is hereby requested that a **one** month extension of time be granted to August 16, 2007 for

- ☒ filing a response to the Official Action dated: April 16, 2007
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

The required fee of \$120.00 is enclosed herewith by check and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.


Bradley D. Lytle
Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/03)

Kurt M. Berger, Ph.D.
Registration No. 51,461

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